

FILE 'USPATFULL' ENTERED AT 09:33:07 ON 29 JAN 2001

L1 29 S LOGICAL COMBINATION (P) ((FIRST SIGNAL# (P) SECOND SIGNAL#)
O
L2 5797 S (TEST CIRCUIT# OR DEVICE#) (P) (MICROCONTROLLER#)
L3 2829 S MICRO(W)CONTROLLER#
L4 6 S L1 AND CONTROLLER#
L5 5 S TEST### (P) L4
SAVE TESTCIRCUIT/L ALL

=> d l4 ibib ti 1-6

L4 ANSWER 1 OF 6 USPATFULL

ACCESSION NUMBER: 2000:15953 USPATFULL
TITLE: Semiconductor memory device for providing burst mode control signal, device comprising plural serial transition registers
INVENTOR(S): Kim, Eun-Cheol, Ohsan, Korea, Republic of
Jung, Chul-Min, Seoul, Korea, Republic of
PATENT ASSIGNEE(S): Samsung Electronics, Co., Ltd., Suwon, Korea, Republic of (non-U.S. corporation)

	NUMBER	DATE
PATENT INFORMATION:	US 6023177	20000208
APPLICATION INFO.:	US 1997-988312	19971211 (8)

	NUMBER	DATE
PRIORITY INFORMATION:	KR 1996-64390	19961211
DOCUMENT TYPE:	Utility	
PRIMARY EXAMINER:	Kim, Jung Ho	
LEGAL REPRESENTATIVE:	Marger Johnson & McCollom, P.C.	
NUMBER OF CLAIMS:	15	
EXEMPLARY CLAIM:	1	
NUMBER OF DRAWINGS:	5 Drawing Figure(s); 2 Drawing Page(s)	
LINE COUNT:	304	

TI Semiconductor memory device for providing burst mode control signal, device comprising plural serial transition registers

L4 ANSWER 2 OF 6 USPATFULL

ACCESSION NUMBER: 96:85490 USPATFULL
TITLE: High-density erasable programmable logic device architecture using multiplexer interconnections
INVENTOR(S): Pedersen, Bruce B., San Jose, CA, United States
PATENT ASSIGNEE(S): Altera Corporation, San Jose, CA, United States (U.S. corporation)

	NUMBER	DATE
PATENT INFORMATION:	US 5557217	19960917
APPLICATION INFO.:	US 1994-331964	19941031 (8)
RELATED APPLN. INFO.:	Continuation of Ser. No. US 1993-123435, filed on 17 Sep 1993, now patented, Pat. No. US 5384499 which is a continuation-in-part of Ser. No. US 1993-43146, filed on 31 Mar 1993, now patented, Pat. No. US 5268598	

which

is a continuation of Ser. No. US 1992-957091, filed on

6 Oct 1992, now abandoned where is a continuation of
Ser. No. US 1991-691640, filed on 25 Apr 1991, now
patented, Pat. No. US 5241224

DOCUMENT TYPE: Utility
PRIMARY EXAMINER: Hudspeth, David R.
LEGAL REPRESENTATIVE: Fish & Neave; Jackson, Robert R.
NUMBER OF CLAIMS: 7
EXEMPLARY CLAIM: 1
NUMBER OF DRAWINGS: 13 Drawing Figure(s); 12 Drawing Page(s)
LINE COUNT: 740
TI High-density erasable programmable logic device architecture using
multiplexer interconnections

L4 ANSWER 3 OF 6 USPATFULL

ACCESSION NUMBER: 96:36881 USPATFULL
TITLE: Personal identification, access control and monitoring
system
INVENTOR(S): McAllister, Clarke W., Corvallis, OR, United States
PATENT ASSIGNEE(S): First Tracks, Eugene, OR, United States (U.S.
corporation)

	NUMBER	DATE
PATENT INFORMATION:	US 5512887	19960430
APPLICATION INFO.:	US 1993-7649	19930122 (8)
RELATED APPLN. INFO.:	Division of Ser. No. US 1991-700215, filed on 13 May 1991, now patented, Pat. No. US 5266783	
DOCUMENT TYPE:	Utility	
PRIMARY EXAMINER:	Zimmerman, Brian	
LEGAL REPRESENTATIVE:	Kolisch Hartwell Dickinson McCormack & Heuser	
NUMBER OF CLAIMS:	18	
EXEMPLARY CLAIM:	1	
NUMBER OF DRAWINGS:	8 Drawing Figure(s); 3 Drawing Page(s)	
LINE COUNT:	943	
CAS INDEXING IS AVAILABLE FOR THIS PATENT.		
TI	Personal identification, access control and monitoring system	

L4 ANSWER 4 OF 6 USPATFULL

ACCESSION NUMBER: 95:78404 USPATFULL
TITLE: Personal identification, access control and monitoring
system
INVENTOR(S): McAllister, Clarke W., Corvallis, OR, United States
PATENT ASSIGNEE(S): First Tracks, Inc., Eugene, OR, United States (U.S.
corporation)

	NUMBER	DATE
PATENT INFORMATION:	US 5446265	19950829
APPLICATION INFO.:	US 1993-107450	19930817 (8)
RELATED APPLN. INFO.:	Continuation of Ser. No. US 1991-700215, filed on 13 May 1991, now patented, Pat. No. US 5266783	
DOCUMENT TYPE:	Utility	
PRIMARY EXAMINER:	Hajec, Donald	
ASSISTANT EXAMINER:	Lee, Michael G.	
LEGAL REPRESENTATIVE:	Kolisch Hartwell Dickinson McCormack & Heuser	
NUMBER OF CLAIMS:	3	
EXEMPLARY CLAIM:	1	
NUMBER OF DRAWINGS:	8 Drawing Figure(s); 3 Drawing Page(s)	
LINE COUNT:	868	
TI	Personal identification, access control and monitoring system	

L4 ANSWER 5 OF 6 USPATFULL

ACCESSION NUMBER: 95:14003 USPATFULL
TITLE: Two-dimensional plural contact array for heading an ID
unit

INVENTOR(S):

McAllister, Clarke W., 2150 NW 25th Pl., Corvallis,
OR, United States 97330

	NUMBER	DATE
PATENT INFORMATION:	US 5389772	19950214
APPLICATION INFO.:	US 1993-108088	19930817 (8)
RELATED APPLN. INFO.:	Continuation of Ser. No. US 1991-700215, filed on 13 May 1991, now patented, Pat. No. US 5266783	
DOCUMENT TYPE:	Utility	
PRIMARY EXAMINER:	Willis, Davis L.	
ASSISTANT EXAMINER:	Sikorski, Edward H.	
LEGAL REPRESENTATIVE:	Kolisch Hartwell Dickinson McCormack & Heuser	
NUMBER OF CLAIMS:	1	
EXEMPLARY CLAIM:	1	
NUMBER OF DRAWINGS:	8 Drawing Figure(s); 3 Drawing Page(s)	
LINE COUNT:	826	
TI	Two-dimensional plural contact array for heading an ID unit	

L4 ANSWER 6 OF 6 USPATFULL

ACCESSION NUMBER: 93:100956 USPATFULL
TITLE: Identification system requiring momentary contact by
limb-worn ID unit with reader detector array
INVENTOR(S): McAllister, Clarke W., Corvallis, OR, United States
PATENT ASSIGNEE(S): First Tracks, Eugene, OR, United States (U.S.
corporation)

	NUMBER	DATE
PATENT INFORMATION:	US 5266783	19931130
APPLICATION INFO.:	US 1991-700215	19910513 (7)
DOCUMENT TYPE:	Utility	
PRIMARY EXAMINER:	Willis, Davis L.	
ASSISTANT EXAMINER:	Sikorski, Edward H.	
LEGAL REPRESENTATIVE:	Kolisch Hartwell Dickinson McCormack & Heuser	
NUMBER OF CLAIMS:	3	
EXEMPLARY CLAIM:	1	
NUMBER OF DRAWINGS:	8 Drawing Figure(s); 3 Drawing Page(s)	
LINE COUNT:	835	
TI	Identification system requiring momentary contact by limb-worn ID unit with reader detector array	